### **Application Data Sheet**

#### **Application Information**

**Application Type::** 

Regular

**Subject Matter::** 

Utility

Suggested classification::

**Suggested Group Art Unit::** 

CD-ROM or CD-R?::

None

Computer Readable Form (CRF)?::

No

Title::

STRAINED SILICON MOSFETS HAVING

NMOS GATES WITH WORK FUNCTIONS

FOR COMPENSATING NMOS

THRESHOLD VOLTAGE SHIFT

**Attorney Docket Number::** 

039153-0688

Request for Early Publication?::

No

Request for Non-Publication?::

Yes

**Suggested Drawing Figure::** 

7g

**Total Drawing Sheets::** 

13

**Small Entity?::** 

No

Petition included?::

No

Secrecy Order in Parent Appl.?::

No

### **Applicant Information**

**Applicant Authority Type::** 

Inventor

**Primary Citizenship Country::** 

US

Status::

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**Applicant Authority Type::** 

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#### **Correspondence Information**

**Correspondence Customer Number::** 

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# Representative Information

Representative Designation::	Registration Number::	Representative
		Name::
Primary	26,257	DAVID A.
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Primary	36,489	RONALD COSLICK
Primary	44,740	IRVIN C.
		HARRINGTON
Primary	41,159	STEPHEN M. LOBBIN
Primary	32,933	TED R. RITTMASTER
Primary	29,430	WILLIAM J.
		ROBINSON
Primary	39,868	ANNA M.
		VRADENBURGH
Primary	36,428	ELIZABETH A.
		APPERLEY
Primary	34,552	BRADLEY BOTSCH
Primary	37,797	MICHAEL CAYWOOD
Primary	33,667	DANIEL R. COLLOPY
Primary	37,296	KENT COOPER
Primary	33,491	PAUL S. DRAKE
Primary	36,654	DIANA ROBERTS
Primary	37,475	KEITH WITEK
Primary	32,638	HARRY A. WOLIN

# **Domestic Priority Information**

Application::	Continuity Type::	Parent	Parent Filing
		Application::	Date::

# **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::

**Assignee Information** 

Assignee name:: Advanced Micro Devices, Inc.